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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/743,958	12/22/2003	Joe A. Harrison	42P17086	9371

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EXAMINER

HYEON, HAE M

ART UNIT PAPER NUMBER

2839

DATE MAILED: 10/15/2004

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary

Application No.

10/743,958

Applicant(s)

HARRISON, JOE A.

Examiner

Hae M Hyeon

Art Unit

2839

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 22 December 2003.
- 2a) ☐ This action is FINAL. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-6 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 1-6 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☒ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 22 December 2003 is/are: a) ☐ accepted or b) ☒ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. _____.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
- * See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- 1) ☒ Notice of References Cited (PTO-892)
- 2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 3) ☐ Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date _____
- 4) ☐ Interview Summary (PTO-413)
Paper No(s)/Mail Date. _____
- 5) ☐ Notice of Informal Patent Application (PTO-152)
- 6) ☐ Other: _____

DETAILED ACTION

Drawings

1. New corrected drawings in compliance with 37 CFR 1.121(d) are required in this application because currently filed drawing is informal drawing. Applicant is advised to employ the services of a competent patent draftsman outside the Office, as the U.S. Patent and Trademark Office no longer prepares new drawings. The corrected drawings are required in reply to the Office action to avoid abandonment of the application. The requirement for corrected drawings will not be held in abeyance.

2. The drawings are objected to as failing to comply with 37 CFR 1.84(p)(4) because reference character "40" has been used to designate power socket, VRM socket and clearance holes. Corrected drawing sheets in compliance with 37 CFR 1.121(d) are required in reply to the Office action to avoid abandonment of the application. Any amended replacement drawing sheet should include all of the figures appearing on the immediate prior version of the sheet, even if only one figure is being amended. The replacement sheet(s) should be labeled "Replacement Sheet" in the page header (as per 37 CFR 1.84(c)) so as not to obstruct any portion of the drawing figures. If the changes are not accepted by the examiner, the applicant will be notified and informed of any required corrective action in the next Office action. The objection to the drawings will not be held in abeyance.

Specification

3. The abstract of the disclosure is objected to because “the die” in line 3 should be -- a die -
- and “the power pod” in line 3 should be -- a power pod --. Correction is required. See MPEP
§ 608.01(b).

Claim Objections

4. Claim 1 is objected to because of the following informalities: Claim 1, line 3, “a
interposer” should be -- an interposer --. Appropriate correction is required.

Claim Rejections - 35 USC § 112

5. The following is a quotation of the second paragraph of 35 U.S.C. 112:

The specification shall conclude with one or more claims particularly pointing out and distinctly claiming the subject matter, which the applicant regards as his invention.

6. Claims 1-6 are rejected under 35 U.S.C. 112, second paragraph, as being indefinite for
failing to particularly point out and distinctly claim the subject matter which applicant regards as
the invention.

The preamble of claims 1-6 recites “motherboard.” However, the body of claims 1-6
does not recite the structure of the motherboard. The body of claim 1 recites the structure of a
device or an apparatus that can be place on the motherboard. The substrate (10), the interposer
(30), and the DC-to-DC converter (50) are not the motherboard, but a device or an apparatus that
can be used with the motherboard. Thus, the preamble of the claims should recites, “A device
comprising” or “An apparatus comprising.”

Claim Rejections - 35 USC § 102

7. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

8. Claims 1-4 and 6 are rejected under 35 U.S.C. 102(b) as being anticipated by Harrison et al (6,392,899 B1).

Harrison discloses a motherboard comprising a substrate 28, an interposer 50 on the substrate 28, a DC-to-DC converter secured on the interposer 50, and contacts 56 located between the interposer 50 and the substrate 28. Although Harrison does not describe the substrate 28 containing power sockets, it is inherent that the substrate 28 contains the power sockets because Figure 4 shows the contacts 56 passing through the substrate 28. Also, Harrison does not disclose the contacts being made of copper. However, copper is the most common material for making contacts of an electronic device.

Claim Rejections - 35 USC § 103

9. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

10. Claim 5 is rejected under 35 U.S.C. 103(a) as being unpatentable over Harrison et al in view of Yamazaki et al (5,975,915).

Claim 5 recites that the contacts are located all around a die on the substrate. While Harrison discloses a die 52, it is not clear whether the contacts 56 are located all around the die since the drawings only show side view of the die 52 and the substrate 28.

Yamazaki discloses an interposer 30 having contacts 42 mounted on a circuit board 20 and a die 10 mounted on the interposer 30. Figure 1 shows that the contacts 42 are located all around the die 10. Figure 2 shows the contacts 42 are placed underneath the interposer 30 and in between the interposer 30 and the circuit board 20.

It would have been obvious at the time the invention was made to a person having ordinary skill in the art to modify the motherboard taught by Harrison such that it would have contacts all around a die as taught by Yamazaki because it only deals modifying the motherboard to accept different type of dies. Dies having contacts all around every side or two opposite sides are commonly known.

Conclusion

11. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure. US Patent Application Publication 2002/0173179 A1 by Ito et al, US Patent No. 6,452,113 B2 by Dibene, II et al and US Patent No. 6,709,277 B2 by Ruttan et al.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Hae M Hyeon whose telephone number is 571-272-2093. The examiner can normally be reached on Mon.-Fri. (8:30-5:30).

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If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Tulsidas C Patel can be reached on (571) 272-2098. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Hae M Hyeon
Primary Examiner
Art Unit 2839

hnh

hnh

Hae Moon Hyeon